

Project Plan

Team Members:

Daryl Damman, Logan Lee, Grant Nordling, Braxton Rokos, Gavin Tersteeg

Project Management Style

A Modified Agile Methodology

- There are no retrospectives held after sprints (or milestones, in this case)
- Daily standups are not held due to time constraints of all members
- Weekly meetings to review progress and set next week's goals
- GitLab to track milestones

Milestones

- **Initial Complex Designs**
 - Certain CPU components cannot be directly translated from FPGA simulation to hardware. As such, these components will need technical discussions with the client and members alike.
- **Breadboard Implementation**
 - Per the client's requirement, the CPU will be implemented on breadboard before constructing a PCB design/set-up. This will be the proof-of-concept using the converted FPGA simulation designs.
- **PCB Implementation**
 - Once the breadboard implementation is complete, PCBs must be designed and ordered. These are required to be hot-swappable with breadboards.
- **Finalized PCB Testing**
 - While PCB testing will be performed throughout implementation, dedicated time has been reserved for ironing out all potential kinks in the PCB system.

Additional Requirements

Tasks to complete outside of getting the barebones processor working.

- Creation of detailed technical operation documentation for future users of the i281 system.
- Design easy to operate user interfaces.
- “Make the processor look cool”

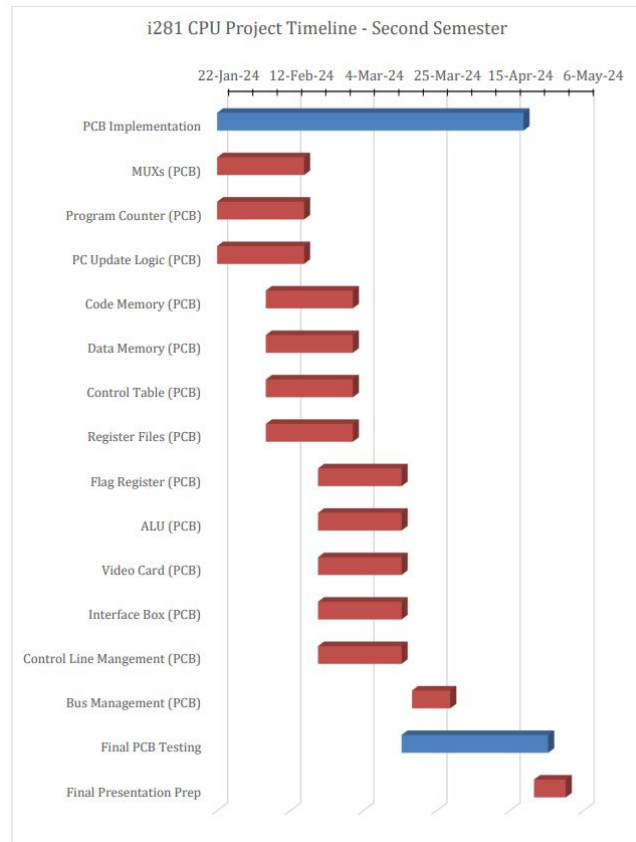
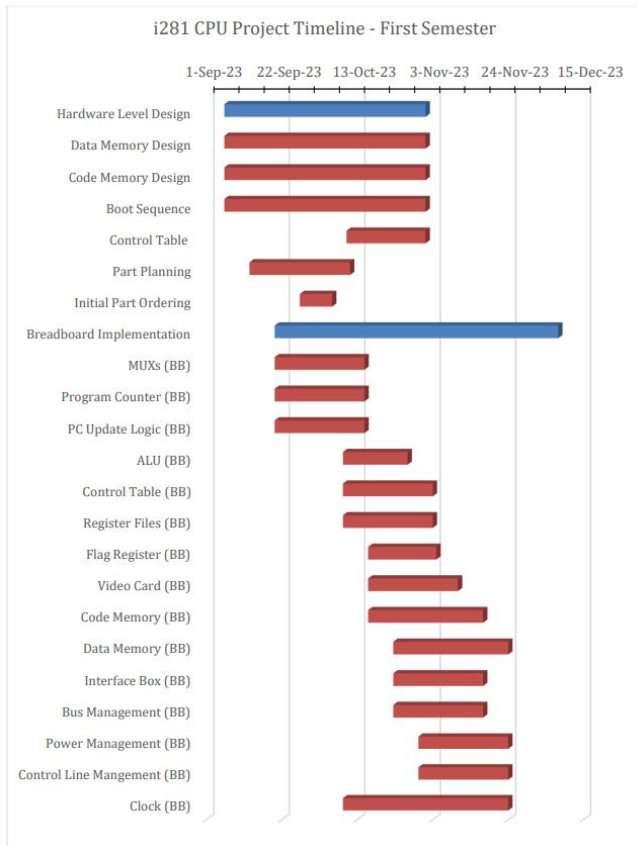
Resources

ETG is our parts supplier and technical support.

Dr. Stoytchev is our client and primary resource for i281 information.

Information on TTL logic and CPU design from previous similar projects.

Support from the CPRE department on the electrical engineering aspect of our project.



Current Project Schedule (Semester 1+2)

Budget

Our budget is approximately \$1000.

Currently, roughly \$350 has been spent.

Remaining budget: \$650

Majority of first costs:

- Breadboards (**many**)
- Latches
- Multiplexers
- Ribbon Cable
- 6 Color Wire Spools

Thank you

Questions?